 

**PROJECT REPORT**

# VLSI IMPLEMENTATION IN HARDWARE SECURITY MODULE BASED ON AES ENCRYPTION METHOD

**20ECPJ801**- **PROJECT PHASE - II**

***Submitted by***

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*in partial fulfillment for award of the degree of*

## BACHELOR OF ENGINEERING

IN

**ELECTRONICS AND COMMUNICATION ENGINEERING**

**SRI SAI RAM ENGINEERING COLLEGE**

(An Autonomous Institution; Affiliated to Anna University, Chennai - 600 025)

## ANNA UNIVERSITY :: CHENNAI 600 025

**APRIL 2024**

# SRI SAIRAM ENGINEERING COLLEGE

**(An Autonomous Institution; A**ffili**ated to Anna University, Chennai -600 025)**

## BONAFIDE CERTIFICATE

Certified that this project report on **“VLSI IMPLEMENTATION IN HARDWARE SECURITY MODULE BASED ON AES ENCRYPTION METHOD”** is the bonafide work of **“AKASH A (412520106007), JAIGANESH P (412520106053) and MAHIZHAN M**

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**- II** Work under my supervision.

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Submitted for VIVA-VOCE EXAMINATION held on:

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